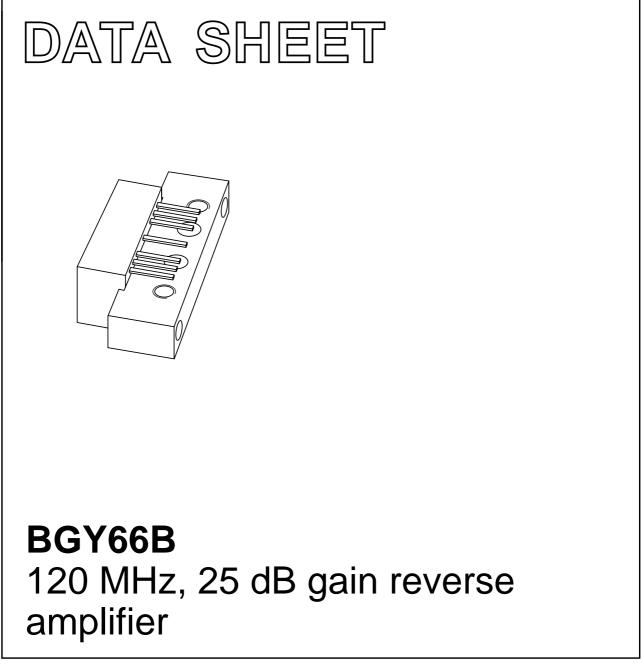
DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1997 Apr 14 2001 Oct 18



Philips Semiconductors

120 MHz, 25 dB gain reverse amplifier

BGY66B

FEATURES

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability.

APPLICATIONS

• Intended as a reverse amplifier for use in two-way systems.

DESCRIPTION

Hybrid high dynamic range amplifier module designed for applications in CATV systems with a bandwidth of 5 to 120 MHz operating with a voltage supply of 24 V (DC).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 10 MHz	24.5	25.5	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	115	135	mA

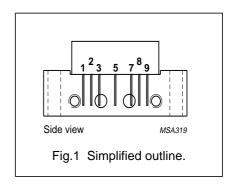
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
Vi	RF input voltage	-	65	dBmV
T _{stg}	storage temperature		+100	°C
T _{mb}	operating mounting base temperature	-20	+100	°C

PINNING - SOT115J

PIN	DESCRIPTION
1	input
2	common
3	common
5	+V _B
7	common
8	common
9	output
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CHARACTERISTICS

Table 1 Bandwidth 5 to 120 MHz; $V_B = 24 V$; $T_{mb} = 30 °C$; $Z_S = Z_L = 75 \Omega$

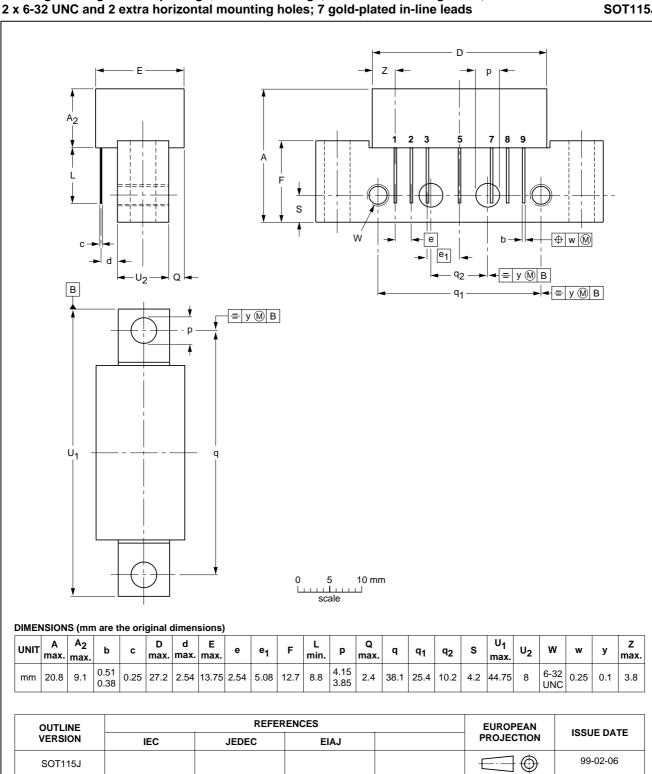
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 10 MHz	24.5	25.5	dB
SL	slope cable equivalent		-0.2	+0.5	dB
FL	flatness of frequency response		-	±0.2	dB
S ₁₁	input return losses		20	-	dB
S ₂₂	output return losses		20	-	dB
СТВ	composite triple beat	14 channels flat; $V_o = 48 \text{ dBmV};$ measured at 67.25 MHz	-	-66	dB
X _{mod}	cross modulation	14 channels flat; $V_o = 48 \text{ dBmV};$ measured at 67.25 MHz	-	-54	dB
d ₂	second order distortion	note 1	-	-70	dB
Vo	output voltage	d _{im} = -60 dB; note 2	60	-	dBmV
F	noise figure	f = 120 MHz	-	5	dB
I _{tot}	total current consumption (DC)	note 3	115	135	mA

Notes

- $\begin{array}{ll} 1. & f_p = 55.25 \; \text{MHz}; \; V_p = 48 \; \text{dBmV}; \\ f_q = 61.25 \; \text{MHz}; \; V_q = 48 \; \text{dBmV}; \\ \text{measured at } f_p + f_q = 116.5 \; \text{MHz}. \end{array}$
- 2. Measured according to DIN45004B: $f_p = 111.25 \text{ MHz}; V_p = V_0;$ $f_q = 118.25 \text{ MHz}; V_q = V_o -6 \text{ dB};$ $f_r = 120.25 \text{ MHz}; V_r = V_o -6 \text{ dB};$ measured at $f_p + f_q - f_r = 109.25 \text{ MHz}.$
- 3. The module normally operates at $V_B = 24$ V, but is able to withstand supply transients up to 30 V.

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes;

PACKAGE OUTLINE



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SOT115J

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DATA SHEET STATUS

DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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